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IMPROVEMENT IN PERFORMANCE OF INDUCTION MOTOR FED BY THREE PHASE PWM VSI USING VARIABLE MODULATION INDEX

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ABSTRACT

Induction motors are most widely used motors for any industrial control and automation. It is often required to control the output voltage of inverter for the constant voltage/frequency (V/F) control of an induction motor. Three phase Pulse Width Modulated Voltage Source Inverters (PWM-VSI) are widely utilized in motor drive and other three-phase power conversion applications as voltage/current sources with controllable output frequency and magnitude. This paper presents the results of an analysis regarding the behavior of three phase induction motor fed by PWM VSI in terms of total harmonic distortion in line and phase voltage of inverter for modulation range 0<m<1. Simulink is utilized with MATLAB to get a reliable and flexible simulation. For the execution of the proposed drive the MATLAB/SIMULINK software has been used. The influence of the modulation index on the performance of the inverter has been done in terms of the waveforms for inverter phase voltage and line voltage developed by the motor. The analysis of the inverter has been carried out by using the factor total harmonic distortion.

Keywords: Induction Motor Drive, Modulation Index, PWM, Switching, Simulink, THD

I. INTRODUCTION

Induction machines are commonly used in the industry, serving as one of the most important roles during the energy conversion between electrical power and mechanical power. Based on the functionality, they are given by two categories: induction motor and induction generator. Induction generators consume the mechanical energy and produce electrical power, which brief out the applications in the standing-alone electrical power generations and wind farms. Induction motors develop mechanical power while consuming the electrical power from the grid, which account for more than one half of the total electrical power consumed. Mainly the induction machines with squirrel cage rotors are the dominant types due to the simple structures, easy connections, lusty to severe operating conditions, low costs, and maintenance free features. They can be organize in the applications as the important driving sources where a certain motion is required either linear or rotating ones. They are utilize to drive, pumps, compressors, power tools, mills, elevators, cranes, fans, electrical vehicles, ships, etc.

DC-AC inverters are electronic devices used to generate AC power from low voltage DC energy (from a battery or solar panel). This makes them very appropriate for when required to use AC power tools or appliances but the
usual AC mains power is not available. Following examples include operating appliances in caravans and mobile homes, and also running audio, video and computing equipment in remote areas. Most inverters do their job by performing two main functions: first they change the incoming DC into AC, and then they set up the resulting AC to mains voltage level using a transformer. And the aim of the designer is to have the inverter perform these functions as efficiently as possible. So that as much as possible of the energy wasted from the battery or solar panel is converted into mains voltage AC, and as little as possible is drawn as heat. Three-phase inverters are employed for variable-frequency drive applications. A basic three-phase inverter contains three single-phase inverter switches each connected to one of the three load terminals.

II. THREE PHASE SPWM INDUCTION MOTOR DRIVE

The Voltage Source Inverter (VSI), is the most widely applied device with 3 power ratings ranging from fractions of a kilowatt to megawatt level among all the modern power electronics converters. The dc source has small or negligible impedance in the Voltage source inverter (VSI). The input terminal voltage is constant. A current–source inverter (CSI) is fed with flexible current from the dc source of high impedance that is from a constant dc source. The six power semiconductor switches with anti-parallel feedback diodes hold by VSI. It converts a fixed DC voltage to three phase AC voltages with governable frequency and magnitude. The VSI has discrete circuit modes for each set of switch states, developing an output voltage with correct frequency and magnitude need an averaging approach. In the widely employ Pulse Width Modulation (PWM) methods, the output voltage inverter approximates the reference value around high frequency switching. In AC motor drive applications, the AC three phase line voltages to DC voltage converted by rectifier device. Following the rectifier voltage passive filtering stage, the PWM-VSI interfaces the DC source with the AC motor to direct the shaft position/torque/speed. Some industrial applications of inverters are for induction heating, UPS for computers, standby aircraft power supplies, HVDC transmission lines, adjustable–speed drives etc. When employed in such applications, the device is often known as converter (opposite of inverter), hence PWM-VSC. In all cases, to obtain high performance, improved efficiency, and reliable operation in a mannered way by the power flow directed by the inverter switching device gate signals. Although its main circuit topology is quite simple, a modern PWM-VSI drive contains an overwhelming level of technology and intelligence. From the semiconductor power switching devices such as frequencies of Insulated Gate Bipolar Transistors (IGBTs) operated at as high as many tens of kilohertz to the microcontrollers and Digital Signal Processors (DSPs) processed the control signals at speeds beyond many tens of megahertz, PWM-VSI drive involved advanced technologies having most components of a state of the art. The people's increasing demand for multifunctionality, precision performance, efficiency, and reliability and user friendliness has encouraged engineers to make a significant amount of intelligence into the microcontrollers and DSPs of the PWM-VSI drives. The modern PWM-VSI drives made up into and generated Load parameter estimation, fault diagnostics, high performance vector control, observer based shaft encoder less speed control, optimization of energy efficiency etc. algorithms. The PWM-VSI drives are modern technology devices with their global production rate above millions per year, the power ratings varies from fractions of a kilowatt to megawatts, and simple house application ranging appliances such as air-conditioning units to heavy industries such as steel mills, which have been experiencing a rapid progress over the last three decades.
Stiff DC voltage source at its input terminals consists of VSI. In other words DC source has small and negligible impedance in Voltage source inverter. With adjustable current from a DC source of high impedance, i.e., from a stiff DC current source is fed by Current Source inverter. In a CSI fed with stiff current source, output current waves are not affected by the load.

It is the first requirement that the output of three phase inverter should be purely sinusoidal. The non-sinusoidal and contain certain harmonics consists of practical inverter waveform. These harmonics are developed by the semiconductor devices employed in implementing three-phase inverter is in the production of harmonics. The power electronics devices utilize in practical circuits like converter/controller circuits generate harmonics in the output voltage supply. The undesirable effects in power supply circuits like generation of humming noises, derating of the machines and torque pulsations which make the operation of the machine pulsating are generated by harmonics. The important parameters which determine the quality of output voltage of inverter are

1. Harmonic Lowest Order
2. Total Harmonic Distortion
3. Distortion Factor
4. Harmonic Factor

In this paper, a technique is implemented to tolerate the fault in the inverter section of a three phase VSI fed induction motor drive. A standard power to three-phase voltage source inverter, fed from a three-phase mains supply, around a three-phase uncontrolled rectifier circuit, commonly supplies the induction motor used in this AC drives category Fig. 1.

Fig. 1. Typical induction motor drive structure based on a voltage source inverter

III. TECHNIQUES USED TO IMPLEMENT INVERTER

Previously 180° conduction mode full bridge type inverter were used so as to generate three phase output voltage waveform. The availability of output voltage is in the form of discrete pulses displaced from each other by an angle of 1200. However the harmonics contents are greater. Inverter gain is lesser in term and the output voltage regulation is poor. The difficulty in satisfaction of the constant voltage and frequency requirement. So as to fulfill above mentioned requirements, Pulse Width Modulation Technique is the most efficient technique required to implement by the three phase inverter. The inverter so fabricated is called PWM Inverter. The width of each pulse is varied on per half cycle basis is done by this technique. The removal of the particular harmonic component from output voltage waveform and to control of output voltage in efficient manner together implemented by the variation of the pulse width in each half cycle. The below PWM techniques used for implementing three phase inverter are
1. Sinusoidal Pulse width modulation
2. Multiple Pulse Width Modulation
3. Single pulse Width Modulation

The harmonic contents can further be reduced by using the number of pulses and modulating the width of these pulses in each half cycle rather than using a single pulse. This methodology implemented by using Multiple Pulse Width Modulation and Sinusoidal pulse Width Modulation techniques [9, 10].

IV. ANALYSIS OF THREE PHASE SPWM VOLTAGE SOURCE INVERTER

The Fig. 2 shows the Simulink diagram of the developed model. Three phase induction motor fed by a PWM inverter constructed in Matlab/Simulink environment its all done by Simulation. For the purpose of simulation, the tool box used is the Sim-power system tool box used in Matlab. The circuit of the proposed scheme contains the three phase induction motor as squirrel-cage rotor type having values as 5.4 HP, 400V, 50 Hz, 1430 RPM. The sinusoidal pulse width modulation technique helps the inverter for generating three phase balanced output.

![Fig. 2.Circuit diagram of PWM based VSI fed Induction motor](image)

The six IGBTs switches in a bridge form is used to design the Voltage source pulse width modulated inverter. IGBT switches has been used because of the number of advantages offered them for switching purpose. Three phase output is taken from the of three arms of bridge circuit designated as a, b, c , which is connected to the three phases a, b, c, on the stator side of three phase induction motor. The output voltage and the load current has been measured in the presence of harmonics. The THD carries out the evolution of harmonics content and the FFT analysis used for extraction of harmonics spectrum.

V. PRODUCTION OF GATING PULSES

The gating pulses for the six IGBTs of three legs are produced. The production of these pulses is carried out by one of the PWM technique that is Sinusoidal Pulse Width Modulation technique. A number of algorithms for PWM voltage generation are obtainable. Some well-known techniques are unipolar voltage switching and bipolar voltage switching. In this work the unipolar switching scheme has been used. The simulation circuit of gate pulses given to the voltage source inverter is shown in Fig. 3.
VI. SIMULATION RESULTS

Results are obtained by simulating the circuit. Here we analyze the inverter and motor performance for modulation range between 0 and 1 i.e. 0 < m < 1. Amplitude Modulation index is defined as the ratio of control signal amplitude and carrier signal amplitude i.e. $m_a = A_r/A_c$. The number of pulses per half cycle depends upon the value of the frequency modulation index $m_f$ defined by the relation $m_f = f_c/(2f)$, where $f_c$ is the frequency of the carrier signal and $f$ is the frequency of the modulating signal.

A. For modulation index $m_a = 0.7$

First of all the inverter is operated in the modulation index i.e. the value of $m_a = 0.7$ is maintained. The generated pulses are applied to the gate circuit of the six IGBTs which in turn produce the balanced pulse width modulated three phase output voltages.

Fig. 4. Phase A current waveform for $m_a = 0.7$

Fig. 4 shows the waveform for the phase “a” current. From the waveform it is clear that the part of the waveform present before the time 0.33 sec is the transient part and after that it acquires its steady state value of $x$ amperes.

Fig. 5. Waveform of line voltage Vab for $m_a = 0.7$

Fig. 5 shows the waveform of line voltage Vab. Similar waveforms can be obtained for the other line voltages Vbc and Vca. From the waveform it is clear that the output waveform is PWM wave and the frequency of the output voltage wave is 50 Hz and its amplitude is 220V.
Fig. 6 FFT analysis in line voltage $V_{ab}$ for $m_a = 0.7$

Fig. 6 shows the frequency spectrum of the line voltage. The value of the THD is 22.36% for the value of $M_a = 0.7$. From the waveforms it is clear that the dominant harmonic is the third harmonic.

Fig. 7 Phase ‘a’ voltage waveform for $m_a = 0.7$

Fig. 7 shows the waveform and Fig. 8 shows frequency spectrum of the phase voltage for $m_a = 0.7$. It shows THD of 17.47%.

B. For modulation index $m_a = 0.8$

For modulation index $m_a = 0.8$, the waveform of line voltage $V_{ab}$ of inverter is shown in Fig. 9. Similar waveforms can be obtained for the other line voltages $V_{bc}$ and $V_{ca}$. From the waveform it is clear that the output waveform is PWM wave and the frequency of the output voltage wave is 50 Hz and its amplitude is 220V.

Fig. 9 Waveform of line voltage $V_{ab}$ for $m_a = 0.8$
Fig. 10 FFT analysis in line voltage Vab for $m_a = 0.8$

Fig. 10 shows the frequency spectrum of the line voltage. The value of the THD is 19.95% for the value of $M_a = 0.8$. From the waveforms it is clear that the dominant harmonic is the third harmonic.

Fig. 11 Phase ‘a’ voltage waveform for $m_a = 0.8$

Fig. 11 shows the waveform and Fig. 12 shows frequency spectrum of the phase voltage for $m_a = 0.8$. It shows THD of 15.38%.

C. For modulation index $m_a = 0.9$

For modulation index $m_a = 0.9$, the waveform of line voltage $V_{ab}$ of inverter is shown in Fig. 13. Similar waveforms can be obtained for the other line voltages $V_{bc}$ and $V_{ca}$. From the waveform it is clear that the output waveform is PWM wave and the frequency of the output voltage wave is 50 Hz and its amplitude is 220V.

Fig. 13 Waveform of line voltage Vab for $m_a = 0.9$
Fig. 14 FFT analysis in line voltage $V_{ab}$ for $m_a = 0.9$

Fig. 14 shows the frequency spectrum of the line voltage. The value of the THD is 17.55% for the value of $M_a = 0.9$. From the waveforms it is clear that the dominant harmonic is the third harmonic.

Fig. 15 Phase ‘a’ voltage waveform for $m_a = 0.8$

Fig. 15 shows the waveform and Fig. 16 shows frequency spectrum of the phase voltage for $m_a = 0.9$. It shows THD of 14.80%.

VII. COMPARISON OF LINE AND PHASE VOLTAGE FOR VARIOUS MODULATION INDEX

<table>
<thead>
<tr>
<th>S. No.</th>
<th>$M_a$</th>
<th>Line Voltage</th>
<th>Phase Voltage</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.7</td>
<td>22.36</td>
<td>17.74</td>
</tr>
<tr>
<td>2</td>
<td>0.8</td>
<td>19.95</td>
<td>15.38</td>
</tr>
<tr>
<td>3</td>
<td>0.9</td>
<td>17.55</td>
<td>14.80</td>
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VIII. CONCLUSION

The paper presents improvement in performance analysis of three phase induction motor fed by PWM voltage source inverter in under variable modulation range. The analysis has been carried in the modulation range 0 to 1.
The effect the modulation index on the motor performance in terms of the line voltage and phase voltage has been carried out. Its impact on the performance of the motor in terms of the transients and steady state response has been presented. The main positive point of this paper is that it reviles the performance or behavior of the three phase VSI as well as as the motor for the entire range of under modulation index. There is appreciable improvement in THD in inverter line and phase voltage as the modulation index is rises up and close to unity. Total harmonic distortion THD in line and phase voltage lowered as the value of modulation index rises its level.

REFERENCES


