



POWER FLOW IMPROVEMENT IN TRANSMISSION LINE USING UPFC

B.Rajesh

Assistant professor EEE Department, Dhruva Institute of Engineering and Technology (India)

ABSTRACT

This paper proposes a new real and reactive power coordination controller for a unified power flow controller (UPFC). The basic control for the UPFC is such that the series converter of the UPFC controls the transmission line real/reactive power flow and the shunt converter of the UPFC controls the UPFC bus voltage/shunt reactive power and the DC link capacitor voltage. In steady state, the real power demand of the series converter is supplied by the shunt converter of the UPFC. To avoid instability/loss of DC link capacitor voltage during transient conditions, a new real power coordination controller has been designed. The need for reactive power coordination controller for UPFC arises from the fact that excessive bus voltage (the bus to which the shunt converter is connected) excursions occur during reactive power transfers. A new reactive power coordination controller has been designed to limit excessive voltage excursions during reactive power transfers. PSCAD-EMTDC simulation results have been presented to show the improvement in the performance of the UPFC control with the proposed real power and reactive power coordination controller.

I. INTRODUCTION

UPFC is the most comprehensive multivariable flexible ac transmission system (FACTS) controller. Simultaneous control of multiple power system variables with UPFC poses enormous difficulties. In addition, the complexity of the UPFC control increases due to the fact that the controlled and the control variables interact with each other. UPFC which consists of a series and a shunt converter connected by a common dc link capacitor can simultaneously perform the function of transmission line real/reactive power flow control in addition to UPFC bus voltage/shunt reactive power control [1]. The shunt converter of the UPFC controls the UPFC bus voltage/shunt reactive power and the dc link capacitor voltage. The series converter of the UPFC controls the transmission line real/reactive power flows by injecting a series voltage of adjustable magnitude and phase angle. The interaction between the series injected voltage and the transmission line current leads to real and reactive power exchange between the series converter and the power system. Under steady state conditions, the real power demand of the series converter is supplied by the shunt converter. But during transient conditions, the series converter real power demand is supplied by the dc link capacitor. If the information regarding the series converter real demand is not conveyed to the shunt converter control system, it could lead to collapse of the dc link capacitor voltage and subsequent removal of UPFC from operation. Very little or no attention has been given to the important aspect of coordination control between the series and the shunt converter control systems [2]–[15]. The real power coordination discussed in [15] is based on the known fact that the shunt converter should provide the real power demand of the series converter. In this case, the series converter

provides the shunt converter control system an equivalent shunt converter real power reference that includes the error due to change in dc link capacitor voltage and the series converter real power demand. The control system designed for the shunt converter in [15] causes excessive delay in relaying the series converter real power demand information to the shunt converter. This could lead to improper coordination of the overall UPFC control system and subsequent collapse of dc link capacitor voltage under transient conditions. In this paper, a new real power coordination controller has been developed to avoid instability/excessive loss of dc link capacitor voltage during transient conditions.

In contrast to real power coordination between the series and shunt converter control system, the control of transmission line reactive power flow leads to excessive voltage excursions of the UPFC bus voltage during reactive power transfers. This is due to the fact that any change in transmission line reactive power flow achieved by adjusting the magnitude/phase angle of the series injected voltage of the UPFC is actually supplied by the shunt converter. The excessive voltage excursions of the UPFC bus voltage is due to absence of reactive power coordination between the series and the shunt converter control system. This aspect of UPFC control has also not been investigated [2]–[15]. A new reactive power coordination controller between the series and the shunt converter control system has been designed to reduce UPFC bus voltage excursions during reactive power transfers.

In this paper, a UPFC control system that includes the real and reactive power coordination controller has been designed and its performance evaluated. Sections II and III describes the basic control strategy and control system for a UPFC. Section IV provides the details of the real and reactive power coordination controller. The modeling details of UPFC are given in Section V. Section VI provides the results of the PSCAD-EMTDC simulations with real and reactive power coordination controllers. Brief conclusions are given Section VII.

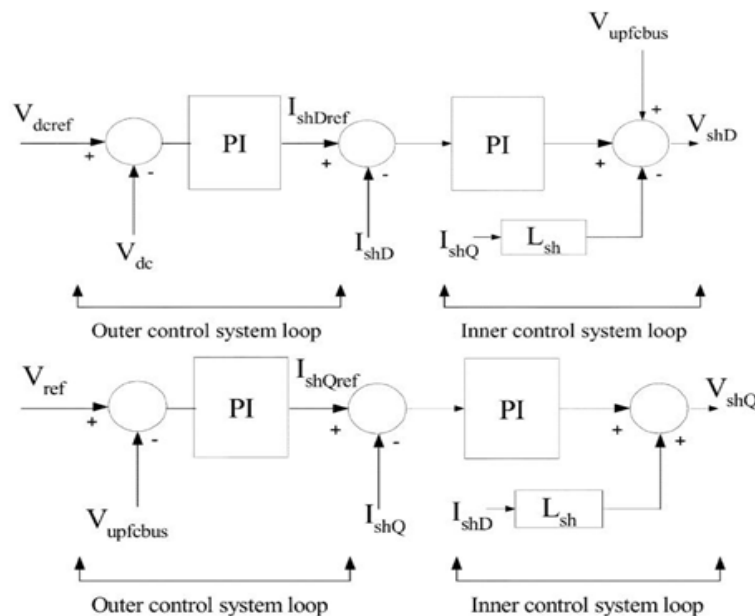


Fig. 1. De-coupled D-Q axis shunt converter control system.

II. CONTROL STRATEGY FOR UPFC

A. Shunt Converter Control Strategy

The shunt converter of the UPFC controls the UPFC bus voltage/shunt reactive power and the dc link capacitor voltage. In this case, the shunt converter voltage is decomposed into two components. One component is in-phase and the other in quadrature with the UPFC bus voltage. De-coupled control system has been employed to achieve simultaneous control of the UPFC bus voltage and the dc link capacitor voltage.

B. Series Converter Control Strategy

The series converter of the UPFC provides simultaneous control of real and reactive power flow in the transmission line. To do so, the series converter injected voltage is decomposed into two components. One component of the series injected voltage is in quadrature and the other in-phase with the UPFC bus voltage. The quadrature injected component controls the transmission line real power flow. This strategy is similar to that of a phase shifter. The in-phase component controls the transmission line reactive power flow. This strategy is similar to that of a tap changer.

III. BASIC CONTROL SYSTEM

A. Shunt Converter Control System

Fig. 1 shows the de-coupled control system for the shunt converter. The D-axis control system controls the dc link capacitor

voltage (V_c) and the Q-axis control system controls the UPFC

bus voltage ($V_{F \text{ bus}}$)/shunt reactive power.

The details of the de-coupled control system design can be found in [16], [17]. The de-coupled control system has been de-signed based on linear control system techniques and it consists of an outer loop control system that sets the reference for the inner control system loop. The inner control system loop tracks the reference.

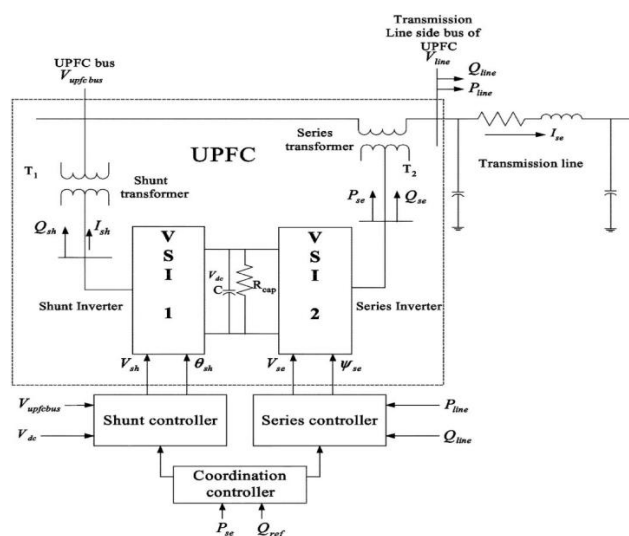


Fig. 2. Series converter real and reactive power flow control system.

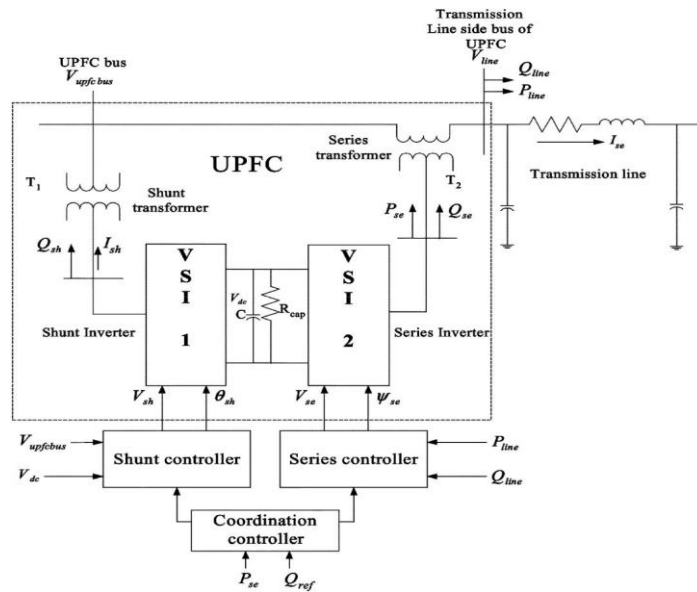


Fig. 3. UPFC connected to a transmission line.

B. Series Converter Control System

transmission line real power flow (P_{line}) is controlled by injecting a component of the series voltage in quadrature with the UPFC bus voltage ($\tilde{V}_e Q$). The transmission line reactive power (Q_{line}) is controlled by modulating the transmission line side bus voltage reference (V_{linef}). The transmission line side bus voltage is controlled by injecting a component of the series voltage in-phase with the UPFC bus voltage ($\tilde{V}_e D$).

IV. REAL AND REACTIVE POWER COORDINATION CONTROLLER

A. Real Power Coordination Controller

To understand the design of a real power coordination controller for a UPFC, consider a UPFC connected to a transmission line as shown in Fig. 3. The interaction between the series injected voltage (\tilde{V}_e) and the transmission line current (\tilde{I}_e) leads

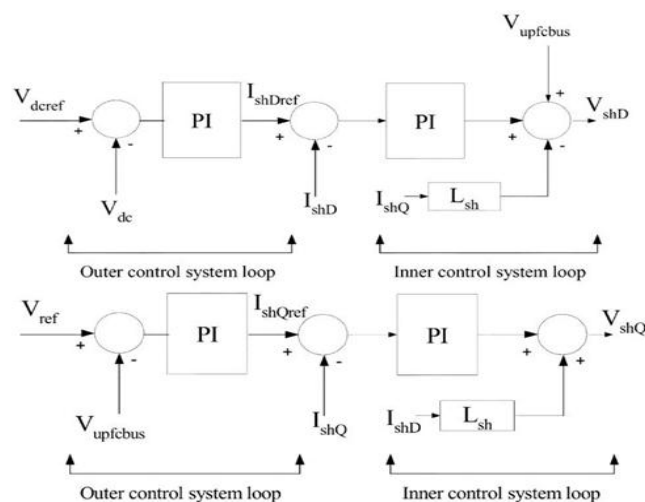


Fig. 4. D-axis shunt converter control system with real power coordination controller.

to exchange of real power (\tilde{P}_e) between the series converter and the transmission line. The real power demand of the series converter (\tilde{P}_e) causes the dc link capacitor voltage (ψ_c) to either increase or decrease depending on the direction of the real power flow from the series converter. This decrease/increase in dc link capacitor voltage (ψ_c) is sensed by the shunt converter controller that controls the dc link capacitor voltage (ψ_c) and acts to increase/decrease the shunt converter real power flow to bring the dc link capacitor voltage (ψ_c) back to its scheduled value. Alternatively, the real power demand of the series converter is recognized by the shunt converter controller only by the decrease/increase of the dc link capacitor voltage (ψ_c). Thus, the shunt and the series converter operation are in a way separated from each other. To provide for proper coordination between the shunt and the series converter control system, a feedback from the series converter is provided to the shunt converter control system. The feedback signal used is the real power demand of the series converter (\tilde{P}_e). The real power demand of the series converter (\tilde{P}_e) is converted into an equivalent D-axis current for the shunt converter (i_{Dse}). By doing so, the shunt converter responds immediately to a change in its D-axis current and supplies the necessary series converter real power demand. The equivalent D-axis current (i_{Dse}) is an additional input to the D-axis shunt converter control system as shown in Fig. 4. Equation (1) shows the relationship between the series converter real power demand (\tilde{P}_e) and the shunt converter D-axis current

$$i_{Dse} = \frac{P_{se}}{|V_{upfc\ bus}|} \quad (1)$$

The real power demand of the series converter \tilde{P}_e is the real part of product of the series converter injected voltage \tilde{V}_e and the transmission line current \tilde{I}_e . $V_{upfc\ bus} i_{Dse}$ represent the voltage of the bus to which the shunt converter is connected and the equivalent additional D-axis current that should flow through the shunt converter to supply the real power demand of the series converter. As shown in Fig. 4, the equivalent D-axis additional current signal (i_{Dse}) is fed to the inner control system, thereby increasing the effectiveness of the coordination

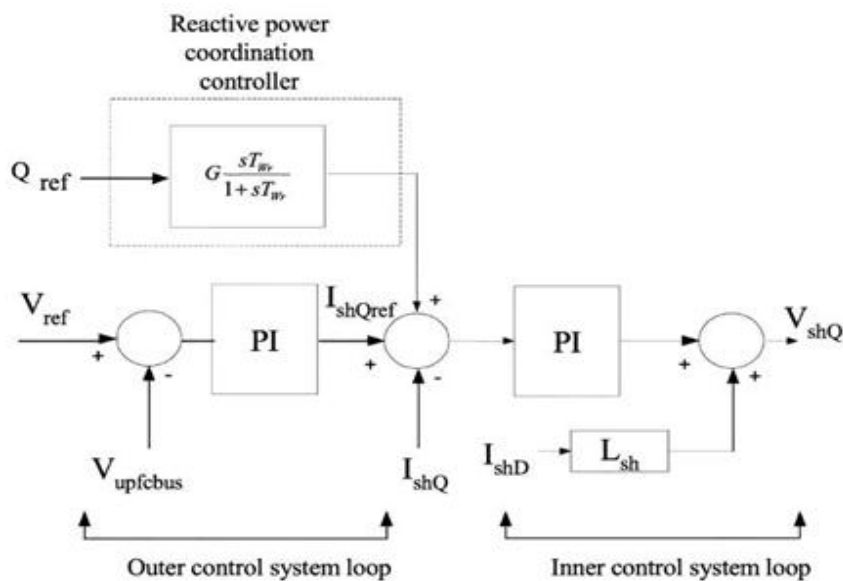


Fig. 5. Shunt converter Q-axis controller with reactive power coordination controller.

controller. Further, the inner control system loops are fast acting PI controllers and ensure fast supply of the series converter real power demand (P_e) by the shunt converter.

B. Reactive Power Coordination Controller

The in-phase component (V_{eD}) of the series injected voltage which has the same phase as that of the UPFC bus voltage, has considerable effect on the transmission line reactive power (Q_{line}) and the shunt converter reactive power (Q_{sh}). Any increase/decrease in the transmission line reactive power (Q_{line}) due to in-phase component (V_{eD}) of the series injected voltage causes an equal increase/decrease in the shunt converter reactive power (Q_{sh}) [19]. In short, increase/decrease in transmission line reactive power is supplied by the shunt converter. Increase/decrease in the transmission line reactive power also has considerable effect on the UPFC bus voltage. The mechanism by which the request for transmission line reactive power flow is supplied by the shunt converter is as follows. Increase in transmission line reactive power reference causes a decrease in UPFC bus voltage. Decrease in UPFC bus voltage is sensed by the shunt converter UPFC bus voltage controller which causes the shunt converter to increase its reactive power output to boost the voltage to its reference value. The increase in shunt converter reactive power output is exactly equal to the increase requested by the transmission line reactive power flow controller (neglecting the series transformer T reactive power loss). Similarly, for a decrease in transmission line reactive power, the UPFC bus voltage increases momentarily. The increase in UPFC bus voltage causes the shunt converter to consume reactive power and bring the UPFC bus voltage back to its reference value. The decrease in the shunt converter reactive power is exactly equal to the decrease in transmission line reactive power flow (neglecting the reactive power absorbed by the series transformer T). In this process, the UPFC bus voltage experiences excessive voltage excursions. To reduce the UPFC bus voltage excursions, a reactive power flow coordination controller has been designed. The input to the reactive power coordination controller is the transmission line reactive power reference. Fig. 5 shows the shunt converter Q-axis control system with the reactive power coordination controller.

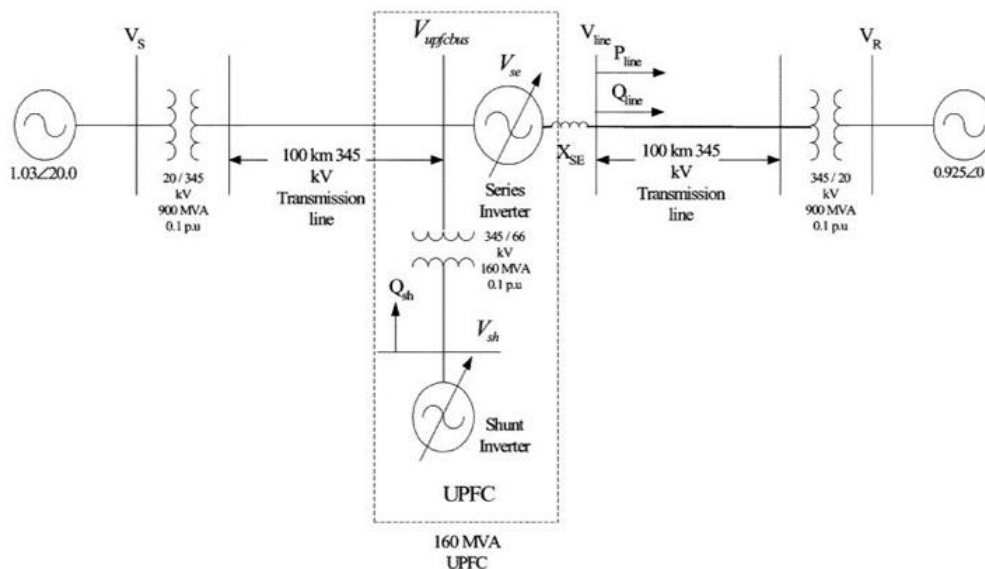


Fig. 6. Power system with UPFC.

The washout circuit represents the reactive power coordination controller. The gain of the washout circuit has been chosen to be 1.0. This is because, any increase/decrease in the transmission line reactive power flow due to change in its reference is supplied by the shunt converter. The washout time constant is designed based on the response of the power system to step changes in transmission line reactive power flow without the reactive power coordination controller.

V. UPFC SIMULATION MODEL

PSCAD-EMTDC software has been used to model the UPFC. The shunt converter has been modeled as a 4-module converter. The series converter consists of two sets of converters. One set of converter is used for the real power flow control and the other set of converter is used for the reactive power flow control. Recent advances in high voltage IGCT technology allow for higher switching frequencies with lower losses. This allows for practical implementation of PWM control [18]. The switching frequency for the converters has been chosen to be nine times the fundamental. A rating of 160 MVA has been assumed for the shunt transformer and series transformer. The UPFC parameters are given in Appendix B.

VI. SIMULATION RESULTS

A. Response to Step Changes in Transmission Line Real Power Flow Reference

Fig. 6 shows the power system with UPFC considered to study the response of the power system to step changes in transmission line real power flow reference. The UPFC is located at the center of a 200 km 345 kV transmission line. The initial real power flow in the transmission line is 450 MW. At 10 s the transmission line real power reference is changed from 450 to 290 MW. At 12 s, the reference is changed from 290 to 450 MW. Plot-1 through Plot-3 of Fig. 7 shows the transmission line real power flow for step changes in its reference. Plot-2 and plot-3 are the enlarged version of plot-1 around 10 s and 12 s, respectively.

B. Response of the Power System to Step Changes in Reactive Power Flow Reference

Plot-1 through plot-8 of Fig. 8 shows the response of the power system (Fig. 6) to step changes in transmission line reactive-

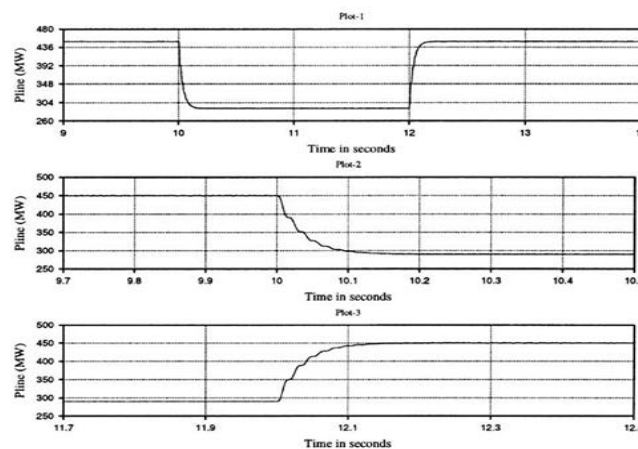
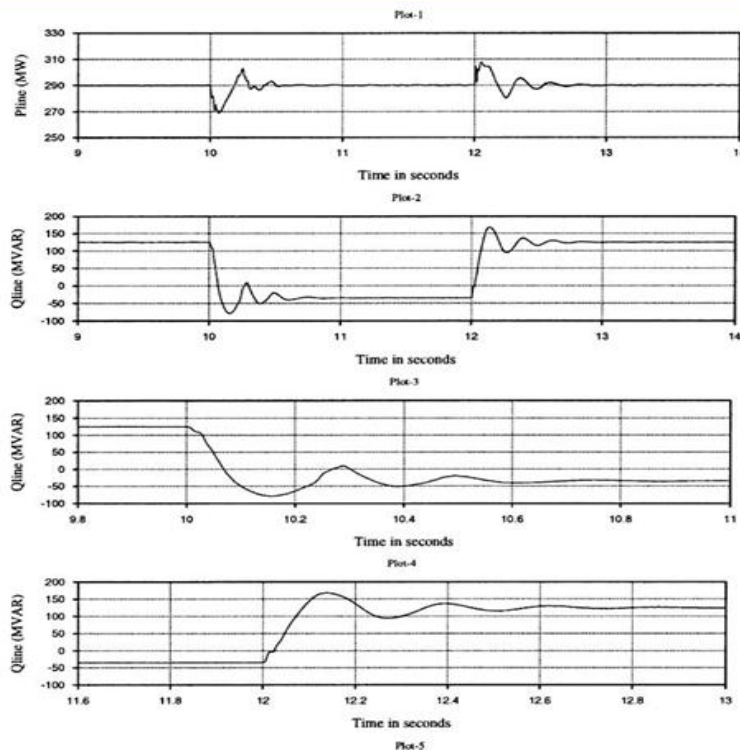


Fig. 7. Response of power system to step changes in transmission line real power reference.

(V_{up}) P
 tive power reference (Q_{ef}). The initial real and reactive power flow ($P_{ine}Q_{line}$) in the transmission line are 290 MW and 125 MVAR, respectively. The initial shunt converter reactive power (Q_{sh}) is 80 MVAR. Step changes of 160 MVAR in transmission line reactive power reference are conducted at 10 s and 12 s. Comparing Plot-3, Plot-4 with Plot-7 and Plot-8, respectively, it is seen that the decrease/increase in transmission line reactive power is balanced by an equal decrease/increase in shunt converter reactive power. It is seen from plot-5 of Fig. 8 that at 10 s, the UPFC bus voltage ($V_{f bus}$) shoots to 1.05 p.u. momentarily for a step decrease of 160 MVAR in transmission line reactive power reference (Q_{ef}). Similarly at 12 s, the UPFC bus voltage ($V_{f bus}$) drops to 0.95 p.u. momentarily for a step increase of MVAR in transmission line reactive power flow (Q_{line}). Accordingly, the shunt converter control system senses the momentary changes in UPFC bus voltage ($V_{f bus}$) and changes its reactive power output (Q_{sh}) to bring the UPFC bus voltage ($V_{f bus}$) to its reference value. The sluggish response of the shunt converter reactive power (Q_{sh}) is due to slow change in UPFC bus voltage. The shunt converter reactive power (Q_{sh}) changes its output with a time constant of about 80 ms ($1/5$ th of 400 ms). This information is used to design the washout time constant of the reactive power coordination controller.

C. Response of the Power System to Step Changes in Transmission Line Reactive Power Reference With Reactive Power Coordination Controller

The input to the reactive power coordination controller is the transmission line reactive power reference (Q_{ef}). The output of the reactive power coordination controller modulates the shunt converter reactive current reference ($I_{hQ_{ef}}$). The time constant T_r has been chosen to be 0.5 s, which is close to the settling time for the shunt converter reactive power (Fig. 8, plot-7 and plot-8). By doing so, the change in effective shunt converter reactive current reference ($I_{hQ_{ef}}$ + output from reactive coordination controller) would take a longer time to decay allowing



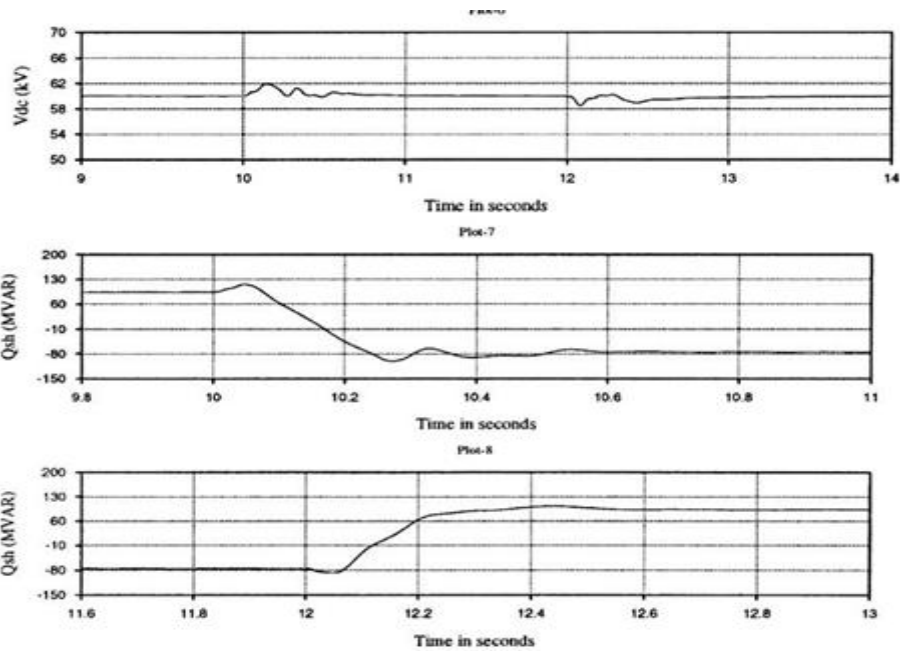


Fig. 8. Response to step change in reactive power reference.

for sufficient time for the outer loop UPFC bus voltage controller to react. This helps in reducing the UPFC bus voltage ($V_{f \text{ bus}}$) excursion during step changes in transmission line reactive power reference ($Q_{e f}$). Fig. 9 shows the comparison of the UPFC bus voltage ($V_{f \text{ bus}}$) and transmission line reactive power with and without the reactive power coordination controller. With the reactive power coordination controller, the UPFC bus voltage excursion has reduced significantly. The impact of reactive

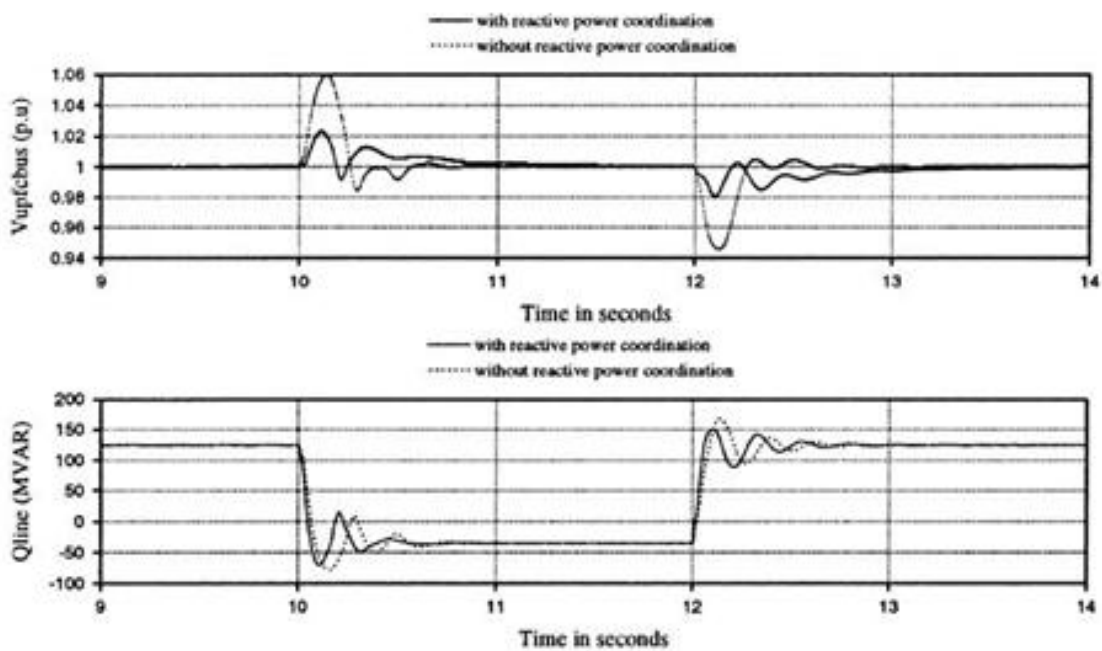


Fig. 9. Impact of reactive power coordination controller.

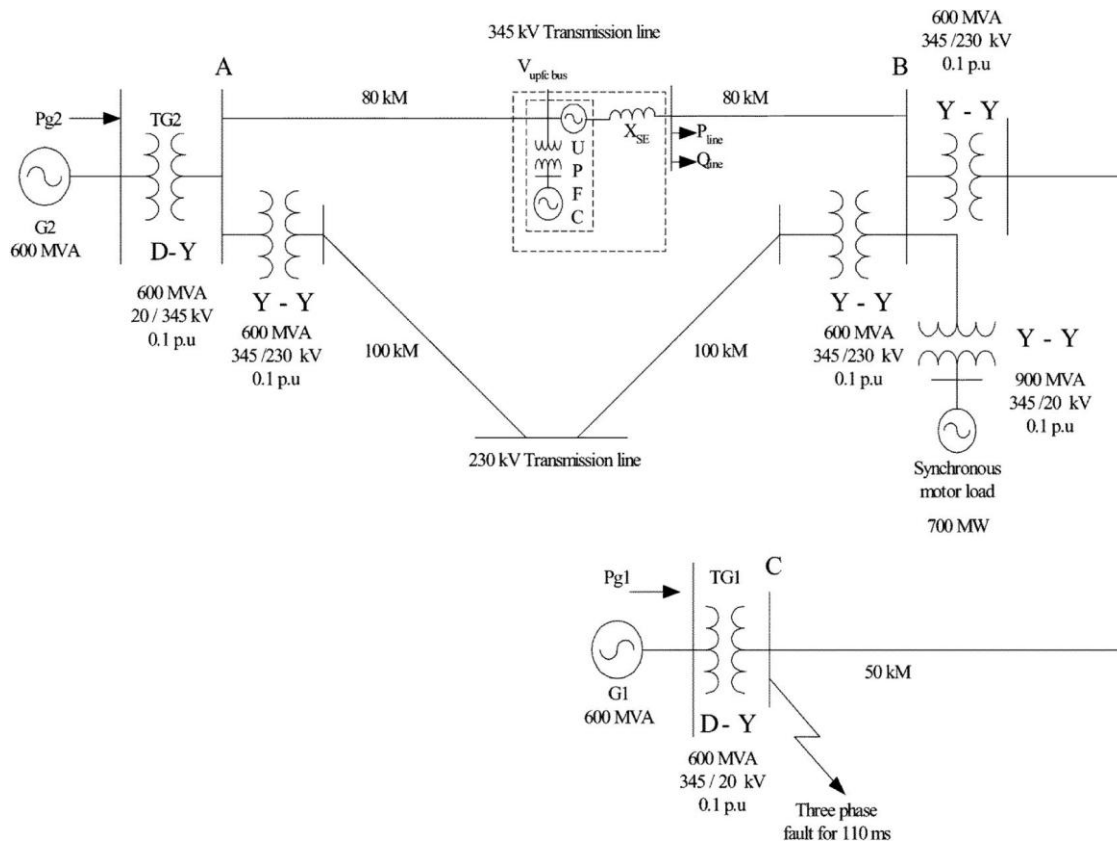


Fig. 10. Power system with UPFC.

power coordination controller on transmission line reactive power flow is minimal.

D. Effect of Real Power Coordination Controller

To study the efficacy of the real power coordination controller, a power system shown in Fig. 10 has been considered. The machines are equipped with static exciters and PSS. The generator, exciter, PSS, synchronous motor load, and UPFC parameters are given in Appendix B [21]. The total load in the power system is 700 MW. The load has been modeled as a synchronous motor. Generator G2 supplies 500 MW of power and the rest of the power is generated by G1. Generator G1 also supplies the system losses. The steady-state power flow in the 345 kV transmission line is 400 MW. The 230 kV transmission line carries 100 MW of power. The UPFC is located at the center of a 160-km 345-kV line.

The shunt converter of the UPFC controls the dc link capacitor voltage (V_{dc}) and the shunt converter reactive power. The series converter of the UPFC controls the real power flow in the transmission line (P_{line}) at 400 MW and the reactive power flow at 100 MVAR. A three-phase fault is conducted at 12 s for 50 ms at bus-B with no change in network configuration. Plot-1 and Plot-2 (enlarged version of Plot-1) of Fig. 11 shows the dc link capacitor voltage with and without the real power coordination controller. The three-phase fault causes the real power to

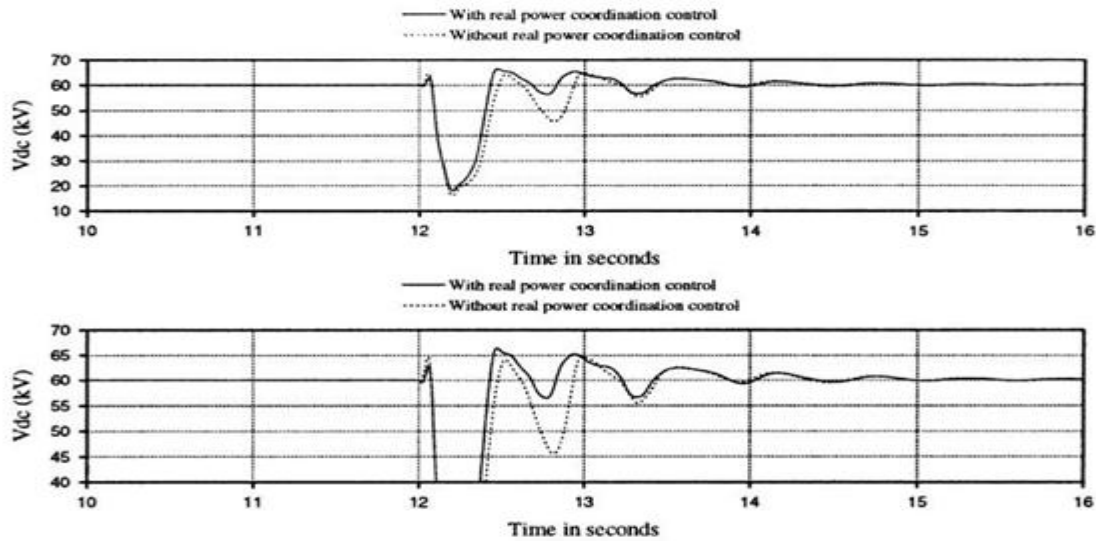


Fig. 11. Impact of real power coordination controller.

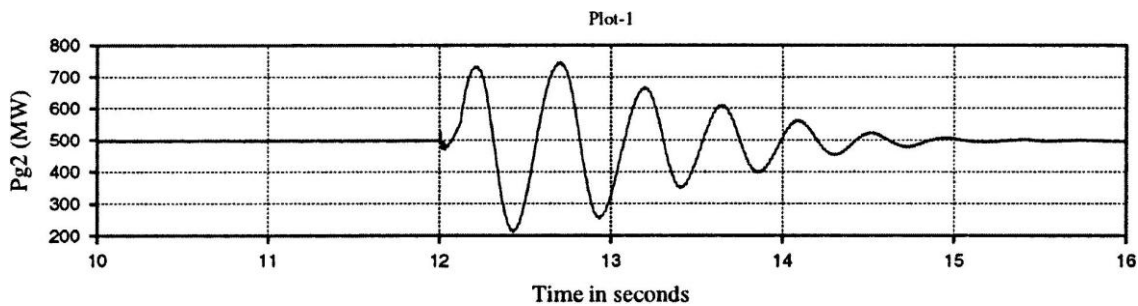


Fig. 12. Response of the power system to three-phase fault without UPFC.

be generated by the UPFC leading to reduction in dc link capacitor voltage. The dc link capacitor voltage drops to about 20 kV. Following the fault removal, the dc link capacitor is charged up by the shunt converter. It is evident from plot-1 and plot-2 of Fig. 11 that the real power coordination controller has significantly improved the recovery of the dc link capacitor voltage under transient conditions. Further, the dc link capacitor voltage oscillations are well damped with real power coordination controller.

E. Power Oscillation Damping

A two-machine (G1 and G2) power system shown in Fig. 10 has been modeled in PSCAD-EMTDC to study the improvement in power oscillation damping of generator G2 using a UPFC. A three-phase fault is applied at the high voltage bus of generator G1 (bus-C) at 12 s for 110 ms and removed without any change in the network configuration. Fig. 12 shows the electrical power (P_2) oscillations of the generator G2 without the UPFC for the three-phase fault.

To show the improvement in power oscillation damping with UPFC, the UPFC is located at bus-A as shown in Fig. 10. In this case, the shunt converter of the UPFC controls the dc link capacitor-

itor voltage (V_c) at 60 kV and the shunt reactive power at zero. The series converter of the UPFC controls the real power flow

in the transmission line (P_{line}) at 400 MW. The reactive power flow controller has been disabled for this simulation as it does not contribute much to real power oscillation damping. Linear control analysis has been used to design the control system for the series converter [20].

Plot-1 through plot-4 of Fig. 13 shows the response of the system with UPFC for a three phase fault at bus-C for 110 ms. The generator G2 oscillations are well damped. The UPFC has regulated the dc link voltage at 60 kV, the shunt converter reac-tive power at zero and the transmission line real power flow to 400 MW.

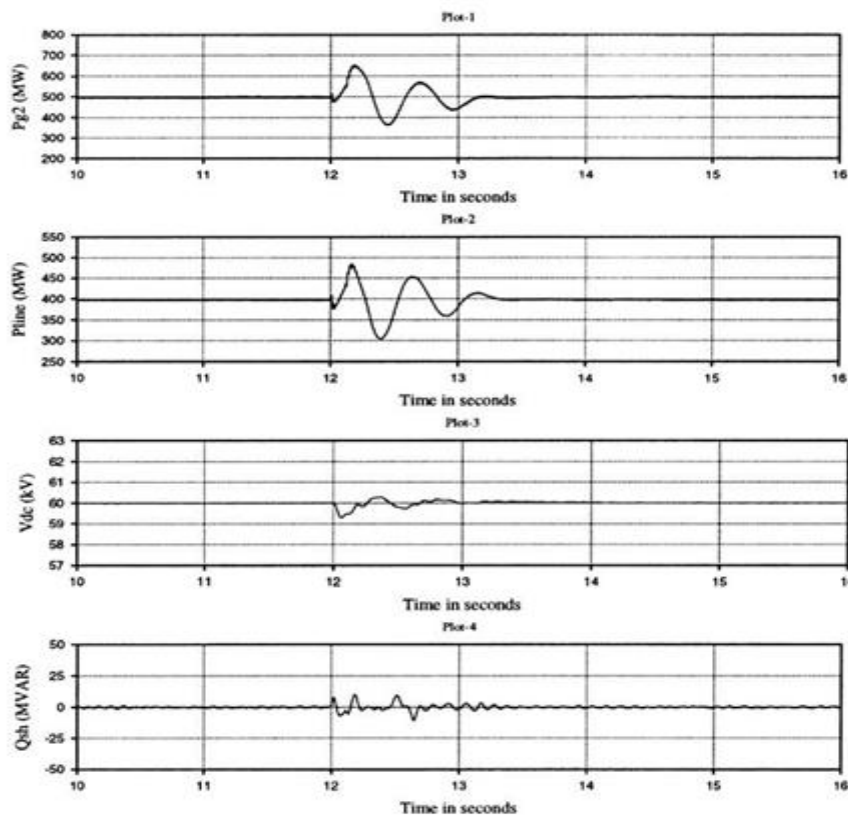


Fig. 13.Response of the power system to three-phase fault with UPFC.

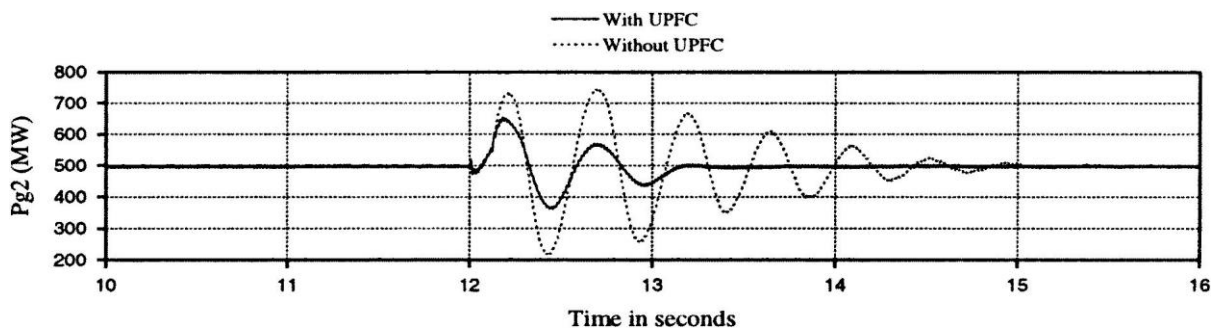


Fig. 14.Generator G2 electrical power with and without UPFC.

generator G2 electrical power (P_2)_g with UPFC and without UPFC. It is evident from Fig. 14 that the UPFC has significantly improved the damping of generator G2 power oscillations.

VII. CONCLUSION

This paper has presented a new real and reactive power co-ordination controller for a UPFC. The basic control strategy is such that the shunt converter of the UPFC controls the UPFC bus voltage/shunt reactive power and the dc link capacitor voltage. The series converter controls the transmission line real and re-active power flow. The contributions of this work can be sum-marized as follows.

Two important coordination problems have been addressed in this paper related to UPFC control. One, the problem of real power coordination between the series and the shunt converter control system. Second, the problem of excessive UPFC bus voltage excursions during reactive power transfers requiring re-active power coordination.

Inclusion of the real power coordination controller in the UPFC control system avoids excessive dc link capacitor voltage excursions and improves its recovery during transient conditions. PSCAD-EMTDC simulations have been conducted to verify the improvement in dc link voltage excursions during transient conditions.

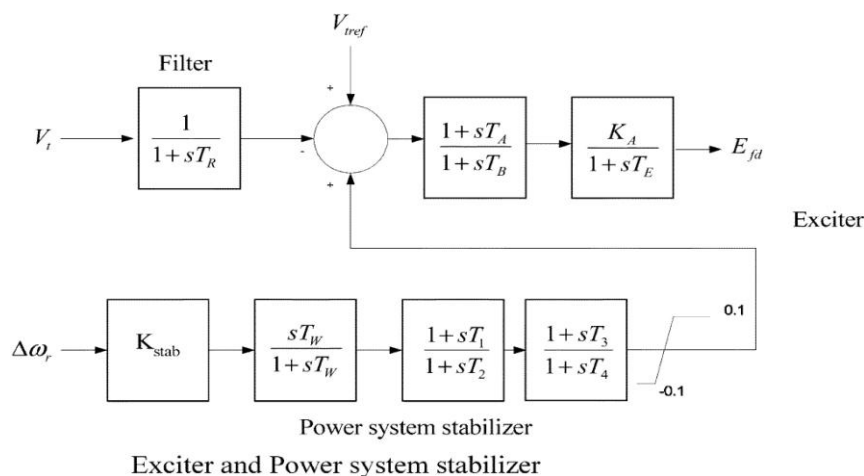


Fig. 15 Exciter and power system stabilizer.

Inclusion of reactive power coordination controller helps in significantly reducing UPFC bus voltage excursions during re-active power transfers. The effect on transmission line reactive power flow is minimal.

PSCAD-EMTDC simulations have shown the improvement in power oscillation damping with UPFC.

VIII. APPENDIX

A. Series Converter Control Parameters

- 1) Transmission line real power flow controller parameters

$$K_p = 0.1 \quad K_I = 4.0.$$

- 2) Transmission line reactive power flow controller param-eters:



- a) Outer loop controller: $K_p = -0.1$ $K_I = -1.0$.
b) Inner loop controller: $K_p = 0.15$ $K_I = 25.0$.

B. Power System Parameters

1) Generator parameters

$$\begin{aligned}L_{adu} &= 1.6 & L_{aqu} &= 1.5 & l &= 0.2 & L_{ad} &= 0.835 L_{adu} \\L_{aq} &= 0.835 L_{aqu} & L_{fd} &= 0.10667 & r_{fd} &= 0.0005658 \\L_{1d} &= 0.1 & r_{1d} &= 0.01768 & L_{1q} &= 0.45652 \\r_{1q} &= 0.01297 & L_{2q} &= 0.05833 & r_{2q} &= 0.021662 \\H(1) &= 3.15 & H(2) &= 3.5.\end{aligned}$$

2) UPFC parameters

Dc link capacitor = $300F\mu$
Shunt converter transformer is rated at
160 MVA, 345/66 kV, $X_{sh} = 0.12\Omega$.
Series converter transformer is rated at 160 MVA.
38.1/66 kV, $X_{SE} = 0.14\Omega$.

3) Exciter and power system stabilizer parameters (see Fig. 15)

$$\begin{aligned}K_{stab} &= 9.5 & T_W &= 10.0 & T_1 &= 0.05 & T_2 &= 0.02 \\T_3 &= 3.0 & T_4 &= 5.4 & T_R &= 0.02 \\K_A &= 200.0 & T_A &= 1.5 & T_B &= 1.0 & T_E &= 0.02.\end{aligned}$$

4) Synchronous motor load parameters:

- a) Rating: 900 MVA, 20 kV.
b) Parameters

$$\begin{aligned}X_{S1} &= 0.14 & X_{MDO} &= 1.445 & X_{23O} &= 0.0 \\X_{3D} &= 0.0437 & X_{2D} &= 0.2004 & X_{MQ} &= 0.91 \\X_{2Q} &= 0.106 & R_{S1} &= 0.0025 & R_{2D} &= 0.00043 \\R_{3D} &= 0.0051 & R_{2Q} &= 0.00842 & H &= 1.0.\end{aligned}$$

REFERENCES

- [1] L. Gyugyi, C. D. Schauder, S. L. Williams, T. R. Reitman, D. R. Torgerson, and A. Edris, "The unified power flow controller: A new approach to power transmission control," *IEEE Trans. Power Delivery*, vol. 10, pp. 1085–1097, Apr. 1995.
- [2] C. D. Schauder, L. Gyugyi, M. R. Lund, D. M. Hamai, T. R. Rietman, R. Torgerson, and A. Edris, "Operation of the unified power flow controller (UPFC) under practical constraints," *IEEE Trans. Power Delivery*, vol. 13, pp. 630–636, Apr. 1998.
- [3] K. K. Sen and E. J. Stacey, "UPFC-Unified Power flow controller: Theory, modeling, and applications," *IEEE Trans. Power Delivery*, vol. 13, pp. 1453–1460, Oct. 1999.



- [4] L.B. A. Renz, A. S. Mehraben, C. Schauder, E. Stacey, L. Kovalsky, Gyugyi, and A. Edris, "AEP unified power flow controller performance," *IEEE Trans. Power Delivery*, vol. 14, pp. 1374–1381, Oct. 1999.
- [5] P. K. Dash, S. Mishra, and G. Panda, "A radial basis function neural network controller for UPFC," *IEEE Trans. Power Syst.*, vol. 15, pp. 1293–1299, Nov. 2000.
- [6] , "Damping multimodal power system oscillation using a hybrid fuzzy controller for series connected FACTS devices," *IEEE Trans. Power Syst*, vol. 15, pp. 1360–1366, Nov. 2000.
- [7] Z. Huang, Y. Ni, F. F. Wu, S. Chen, and B. Zhang, "Application of unified power flow controller in interconnected power systems-modeling, interface, control strategy and case study," *IEEE Trans. Power Syst*, vol. 15, pp. 817–824, May 2000.
- [8] Y. Morioka, Y. Mishima, and Y. Nakachi, "Implementation of unified power flow controller and verification of transmission capability improvement," *IEEE Trans. Power Syst*, vol. 14, pp. 575–581, May 1999.
- [9] P. C. Stefanov and A. M. Stankovic, "Modeling of UPFC operation under unbalanced conditions with dynamic phasors," *IEEE Trans. Power Syst.*, vol. 17, pp. 395–1403, May 2002.
- [10] H. F. Wang, "A unified model for the analysis of facts devices in damping power system oscillations part I: Single machine infinite-bus power systems," *IEEE Trans. Power Delivery*, vol. 12, pp. 941–946, Apr. 1997.